

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

TED LETAVIC ET AL

US 010631

Serial No.: 10/015,640

Group Art Unit: 2814

Filed: DECEMBER 10, 2001

Title: HIGH FREQUENCY HIGH VOLTAGE SILICON-ON-INSULATORS
DEVICE WITH MASK VARIABLE INVERSION CHANNELLAND METHOD FOR

FORMING THE SAME

Commissioner for Patents Washington, D.C. 20231

LETTER TO OFFICIAL DRAFTSMAN

Sir:

Enclosed are (2) two sheets of formal drawings for filing in the above-identified application.

Respectfully submitted,

Steven R. Biren, Reg. 26,531

Attorney

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited this date with the United States Postal Service as first-class mail in an envelope addressed to:

COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C. 20231

on October 4,2002

By Idna Chape